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To: Examiner Hsien-Ming Lee  
Of: Group 2823 USPTO  
From: Timothy M. Honeycutt  
Client/Matter: Serial No.: 10/085,903 (Atty. Dkt. No. AMDI:115410N)  
Date: June 26, 2003

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Andy Wei et al.

§ Group No.: 2823

Serial No.: 10/085,903

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Filed: February 28, 2002

Examiner: Hsien Ming Lee

For: SOI MOSFET JUNCTION  
DEGRADATION USING MULTIPLE  
BURIED AMORPHOUS LAYERS

Atty. Dkt. No.: AMDI:115\HON

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Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

## RESPONSE TO THE OFFICE ACTION MAILED JUNE 4, 2003

This paper is responsive to the Office Action Mailed June 4, 2003. Preliminarily, the Examiner is thanked for conducting a telephone interview on June 18, 2003 regarding the instant Office Action. If the Examiner believes that additional telephone conferences will advance the prosecution of the above-captioned application, the Examiner is invited to contact the undersigned at (281) 379-7999.

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